

Features

- Monolithic 6-line EMI symmetrical I/O low-pass filter
- ESD Protect for input and output of each line
- 0.4mm pin pitch
- Provide ESD protection for each I/O to
IEC 61000-4-2 (ESD) $\pm 17\text{kV}$ (air/contact)
IEC 61000-4-5 (Lightning) 1A (8/20 μs)
- For low operating voltage applications: 5V and below.
- Fast turn-on and Low clamping voltage
- Solid-state silicon-avalanche and active circuit triggering technology
- Dual Flat No-lead (DFN) packaging and small PCB space occupying: $\leq 3.4\text{ mm}^2$
- High reliability provided by monolithic integration
- Lead free package
- Green Part

Applications

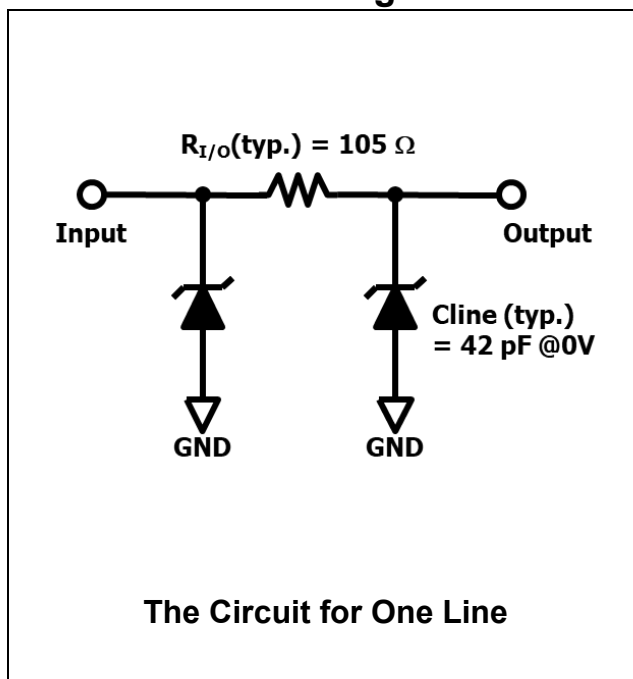
- LCD and camera data lines
- Hand Held Portable Applications: EMI filtering and ESD protection for I/O ports and keypads

Description

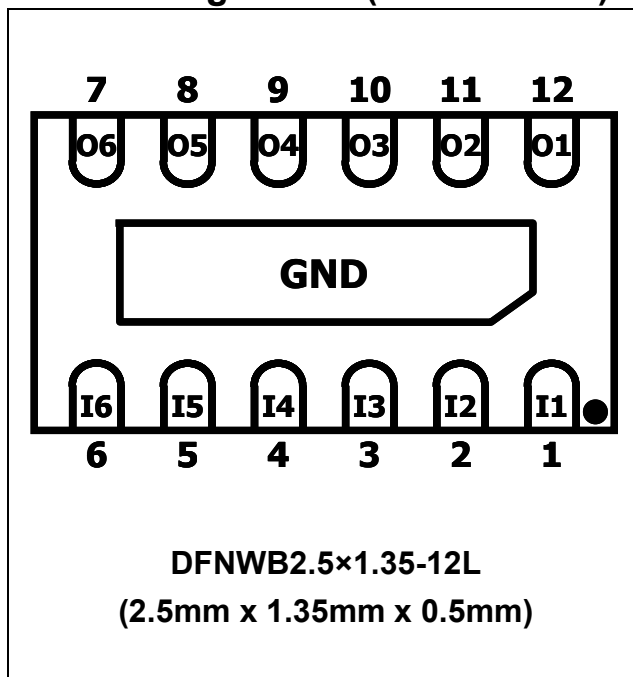
The AZM-LCD07-06F is a 6-line monolithically and highly integrated EMI symmetrical I/O low-pass filter designed to suppress EMI noise in all systems subjected to electromagnetic interferences.

In addition to suppress EMI noise, the AZM-LCD07-06F also includes an ESD protection circuitry at each I/O of each line, which prevents the device from destruction or interference when subjected to ESD stress up to IEC 61000-4-2, Level 4 ($\pm 15\text{kV}$ air, $\pm 8\text{kV}$ contact discharge).

Circuit Diagram



Pin Configuration (Bottom Side)



SPECIFICATIONS

ABSOLUTE MAXIMUM RATINGS			
PARAMETER	SYMBOL	RATING	UNITS
Operating DC Voltage (each I/O to GND)	V_{DC}	5.5	V
Each I/O to GND, ESD per IEC 61000-4-2 (Air/contact)	V_{ESD}	±17	kV
Lead Soldering Temperature	T_{SOL}	260 (10 sec.)	°C
Operating Temperature	T_{OP}	-40 to +125	°C
Storage Temperature	T_{STO}	-55 to +150	°C

ELECTRICAL CHARACTERISTICS						
PARAMETER	SYMBOL	CONDITIONS	MINI	TYP	MAX	UNITS
Reverse Stand-Off Voltage	V_{RWM}	$T=25\text{ }^{\circ}\text{C}$, each I/O to GND.			5	V
Reverse Leakage Current	I_{Leak}	$V_{RWM}=5\text{V}$, $T=25\text{ }^{\circ}\text{C}$, each line to GND.			200	nA
Reverse Breakdown Voltage	V_{BV}	$I_{BV}=1\text{mA}$, $T=25\text{ }^{\circ}\text{C}$, each line to GND.	6		9	V
Forward Voltage	V_F	$I_F=15\text{mA}$, $T=25\text{ }^{\circ}\text{C}$, GND to Each I/O.	0.5	1	1.5	V
ESD Clamping Voltage	V_{clamp}	IEC 61000-4-2 +6kV, $T=25\text{ }^{\circ}\text{C}$, Contact mode, each I/O to GND.		9.5		V
ESD Dynamic Turn-on Resistance	$R_{dynamic}$	IEC 61000-4-2 0~+6kV, $T=25\text{ }^{\circ}\text{C}$, Contact mode, each I/O to GND.		0.15		Ω
Series Resistance between Input and Output	$R_{I/O}$	$T=25\text{ }^{\circ}\text{C}$.	95	105	115	Ω
Input Capacitance per line (0V)	C_{Line_0V}	$V_R=0\text{V}$, $f=1\text{MHz}$, $T=25\text{ }^{\circ}\text{C}$, each line to GND.	35	42	49	pF
Input Capacitance per line (2.5V)	$C_{Line_2.5V}$	$V_R=2.5\text{V}$, $f=1\text{MHz}$, $T=25\text{ }^{\circ}\text{C}$, each line to GND.	20	26	32	pF

Typical Characteristics

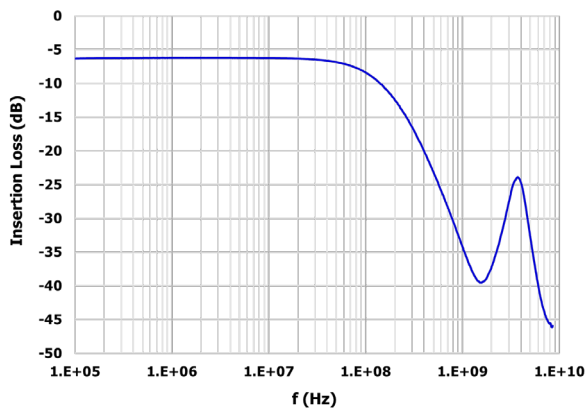


Figure 1. S21 (dB) attenuation measurement.

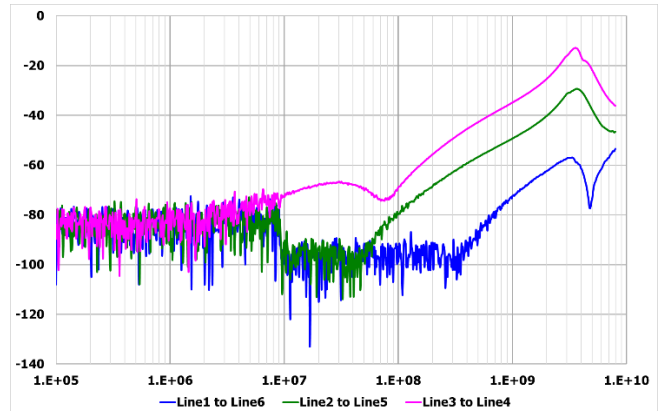


Figure 2. Analog crosstalk measurement.

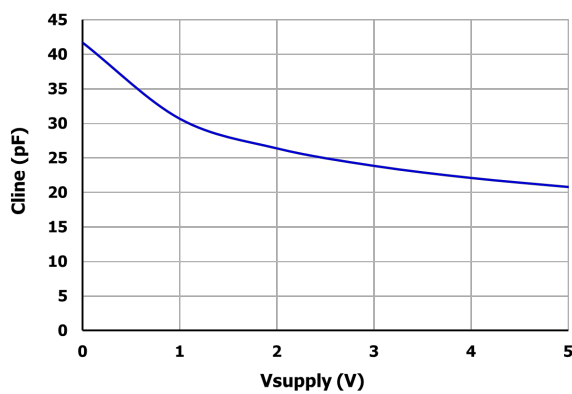


Figure 3. Line capacitance versus applied voltage (typical values).

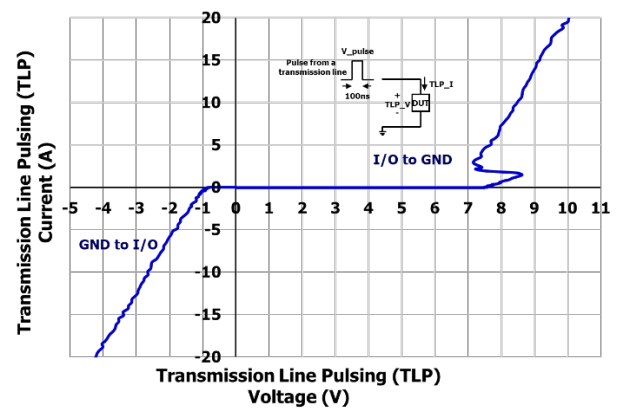
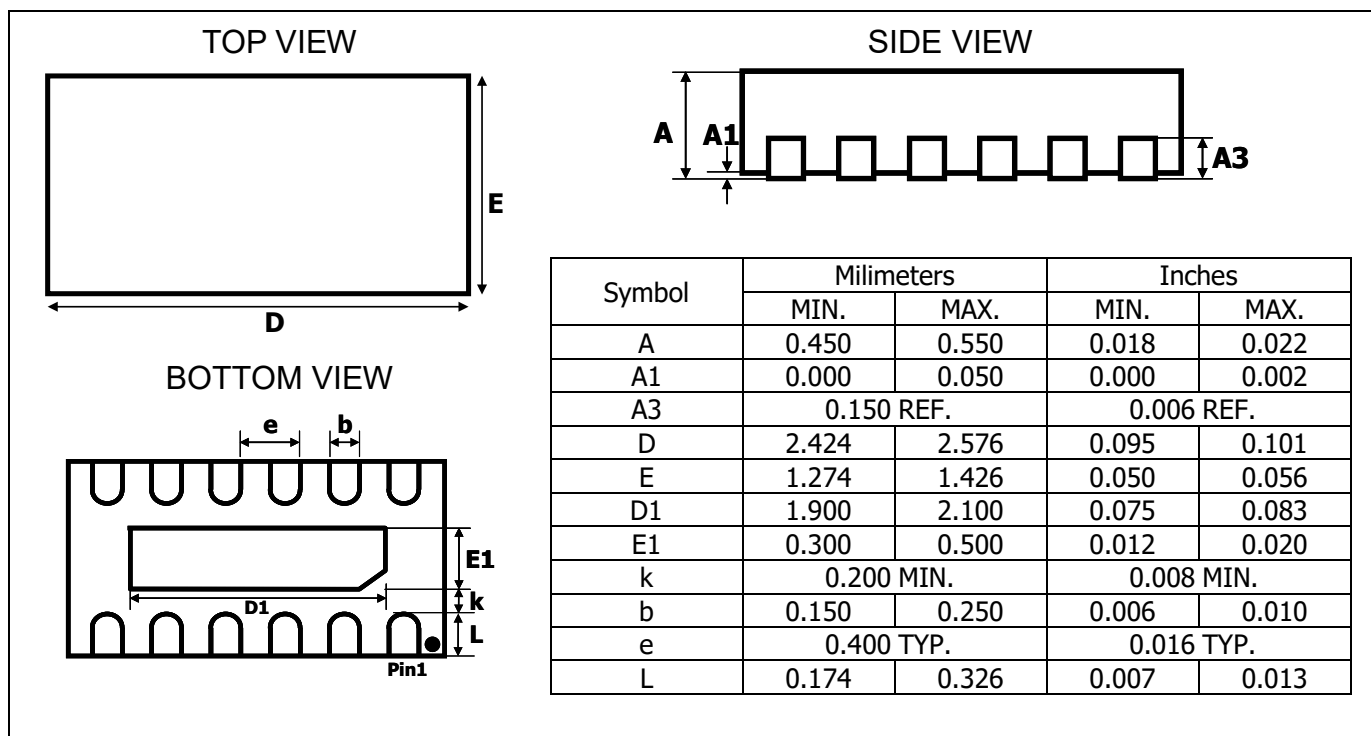


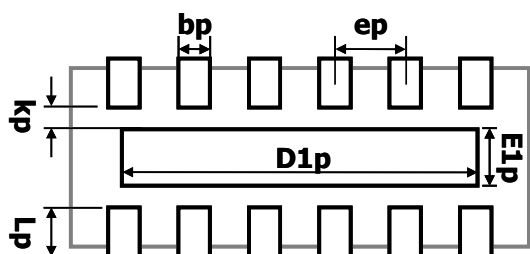
Figure 4. Transmission Line Pulsing (TLP) Measurement for each I/O of each line.

Mechanical Details

DFNWB2.5×1.35-12L PACKAGE DIAGRAMS



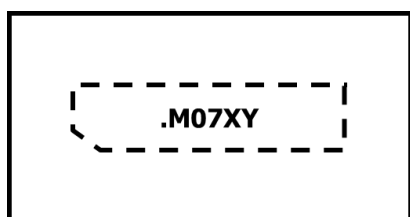
LAND LAYOUT



Symbol	Millimeters	Inches
D1p	2.200	0.087
E1p	0.400	0.016
kp	0.190	0.007
bp	0.220	0.009
ep	0.400	0.016
Lp	0.500	0.020

Notes: This LAND LAYOUT is for reference purposes only.
Please consult your manufacturing partners to ensure what
your company's PCB design guidelines are met.

Marking Code



. = Pin1 orientation
M07 = Device Code
X = Date Code
Y = Control Code

Part Number	Marking Code
AZM-LCD07-06F.R7G	.M07XY

Ordering Information

PN#	Material	Package	Type	Reel size	MOQ	MOQ/internal box	MOQ/carton
AZM-LCD07-06F.R7G	Green	DFN25135P12E	T/R	7 inches	3,000/reel	4 reels=12,000/box	6 boxes=72,000/carton

Revision History

Revision	Modification Description
Revision 2023/08/28	Preliminary Release.