

## Features

- ESD protection for one line with uni-directional
- Provide transient protection for each line to  
IEC 61000-4-2 (ESD)  $\pm 30\text{kV}$  (air / contact)  
IEC 61000-4-4 (EFT)  $\pm 80\text{A}$  (5/50ns)  
IEC 61000-4-5 (Lightning)  $50\text{A}$  (8/20 $\mu\text{s}$ )
- Suitable for, **1.8V and below**, operating voltage applications
- **0402 small MCSP package** saves board space
- Protect one I/O line or one power line
- Fast turn-on and low clamping voltage
- Solid-state silicon-avalanche and active circuit triggering technology
- **Green part**

## Applications

- Power supply protection
- OLED displays
- Small panel modules
- Handheld portable applications
- Low speed data or control line protection
- Peripherals
- Consumer electronics

## Description

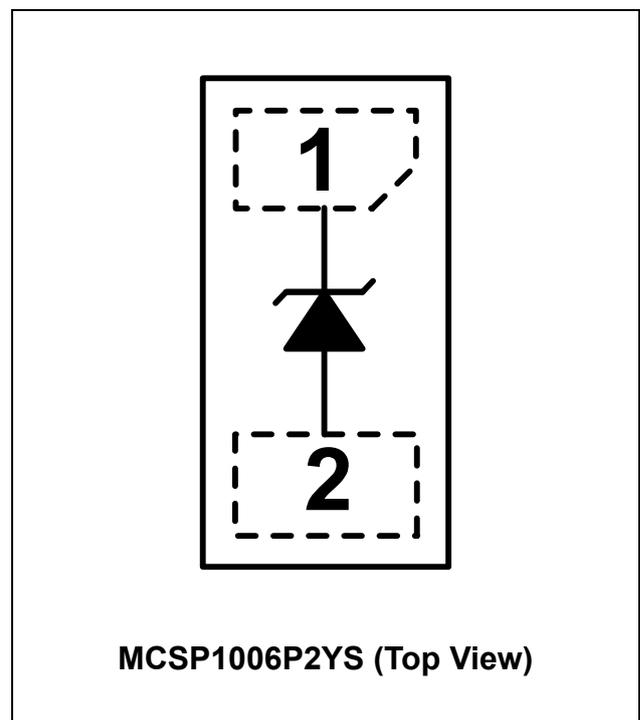
AZ6318-01M is a design which includes an uni-directional surge rated clamping cell to protect one power line, or one control line, or one low speed data line in an electronic system. The AZ6318-01M has been specifically designed to protect sensitive components which are connected to power and control lines from

over-voltage damage caused by Electrostatic Discharging (ESD), Electrical Fast Transients (EFT), Lightning, and Cable Discharge Event (CDE).

AZ6318-01M is a unique design which includes proprietary clamping cell in a single package. During transient conditions, the proprietary clamping cell prevents over-voltage on the power line or control/data lines, protecting any downstream components.

AZ6318-01M may be used to meet the ESD immunity requirements of IEC 61000-4-2, Level 4 ( $\pm 15\text{kV}$  air,  $\pm 8\text{kV}$  contact discharge).

## Circuit Diagram / Pin Configuration





## Specifications

Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$ , unless otherwise specified)			
Parameter	Symbol	Rating	Unit
Peak Pulse Current ( $t_p = 8/20\mu\text{s}$ )	$I_{PP}$ (Note 1)	50	A
ESD per IEC 61000-4-2 (Air)	$V_{ESD-1}$	$\pm 30$	kV
ESD per IEC 61000-4-2 (Contact)	$V_{ESD-2}$	$\pm 30$	
Lead Soldering Temperature	$T_{SOL}$	260 (10 sec.)	$^\circ\text{C}$
Operating Temperature	$T_{OP}$	-55 to +125	$^\circ\text{C}$
Storage Temperature	$T_{STO}$	-55 to +150	$^\circ\text{C}$

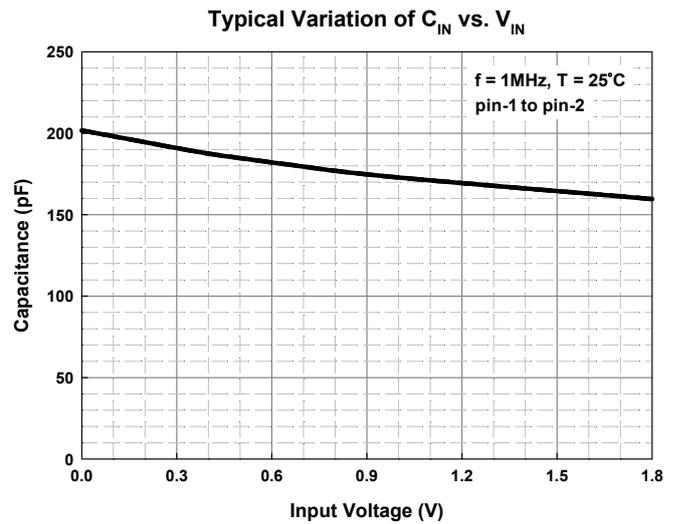
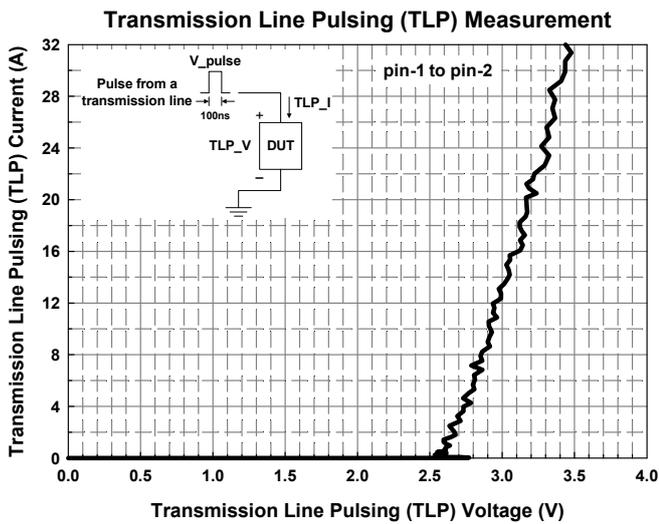
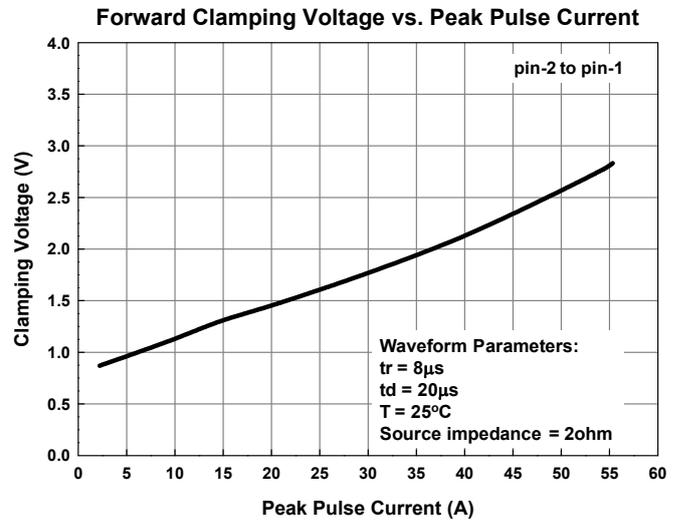
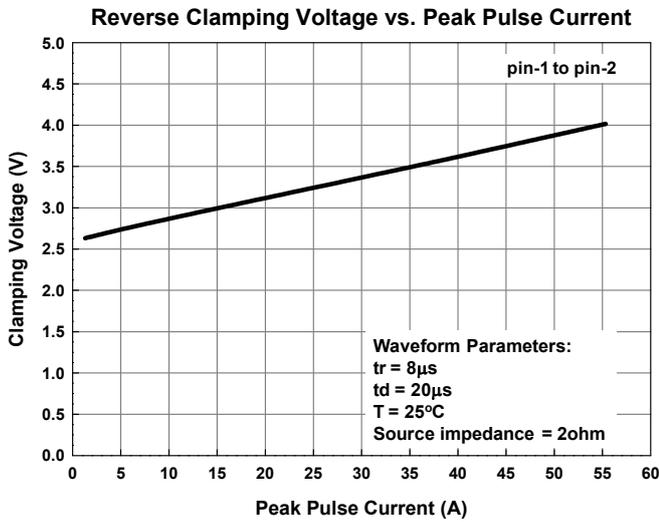
Electrical Characteristics						
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Reverse Stand-Off Voltage	$V_{RWM}$	Pin-1 to pin-2, $T = 25^\circ\text{C}$ .			1.8	V
Reverse Leakage Current	$I_{Leak}$	$V_{RWM} = 1.8\text{V}$ , $T = 25^\circ\text{C}$ , pin-1 to pin-2.			500	nA
Reverse Breakdown Voltage	$V_{BV}$	$I_{BV} = 1\text{mA}$ , $T = 25^\circ\text{C}$ , pin-1 to pin-2.	2.2			V
Forward Voltage	$V_F$	$I_F = 15\text{mA}$ , $T = 25^\circ\text{C}$ , pin-2 to pin-1.	0.6		1.2	V
Surge Clamping Voltage (Note 1)	$V_{CL-Surge}$	$I_{PP} = 5\text{A}$ , $t_p = 8/20\mu\text{s}$ , $T = 25^\circ\text{C}$ , pin-1 to pin-2.		2.7		V
		$I_{PP} = 50\text{A}$ , $t_p = 8/20\mu\text{s}$ , $T = 25^\circ\text{C}$ , pin-1 to pin-2.		4		
ESD Clamping Voltage (Note 2)	$V_{CL-ESD}$	IEC 61000-4-2 +8kV ( $I_{TLP} = 16\text{A}$ ), contact mode, $T = 25^\circ\text{C}$ , pin-1 to pin-2.		3.1		V
ESD Dynamic Turn-on Resistance	$R_{dynamic}$	IEC 61000-4-2 0~+8kV, $T = 25^\circ\text{C}$ , contact mode, pin-1 to pin-2.		0.03		$\Omega$
Channel Input Capacitance	$C_{IN}$	$V_R = 0\text{V}$ , $f = 1\text{MHz}$ , $T = 25^\circ\text{C}$ , pin-1 to pin-2.		200	250	pF

Note 1: The Peak Pulse Current measured conditions:  $t_p = 8/20\mu\text{s}$ ,  $2\Omega$  source impedance.

Note 2: ESD Clamping Voltage was measured by Transmission Line Pulsing (TLP) System.

TLP conditions:  $Z_0 = 50\Omega$ ,  $t_p = 100\text{ns}$ ,  $t_r = 1\text{ns}$ .

## Typical Characteristics



## Applications Information

The AZ6318-01M is designed to protect one line against system ESD/EFT/Lightning pulses by clamping it to an acceptable reference.

The usage of the AZ6318-01M is shown in Fig. 1. Protected line, such as data line, control line, or power line, is connected to pin-1. The pin-2 is connected to a ground plane on the board. In order to minimize parasitic inductance in the board traces, all path lengths connected to the pins of AZ6318-01M should be kept as short as possible.

In order to obtain enough suppression of ESD induced transient, good circuit board is critical. Thus, the following guidelines are recommended:

- Minimize the path length between the protected lines and the AZ6318-01M.
- Place the AZ6318-01M near the input terminals or connectors to restrict transient coupling.
- The ESD current return path to ground should be kept as short as possible.
- Use ground planes whenever possible.
- NEVER route critical signals near board edges and near the lines which the ESD transient easily injects to.

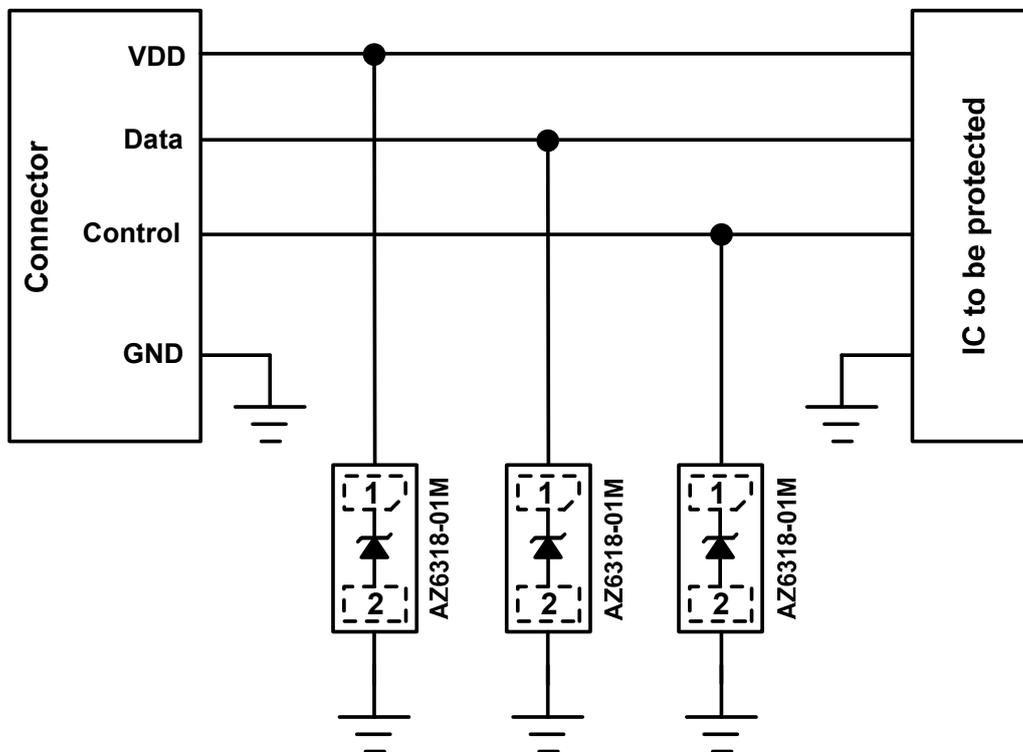


Fig. 1

Fig. 2 shows another simplified example of using AZ6318-01M to protect the control line, low speed data line, and power line from ESD transient stress.

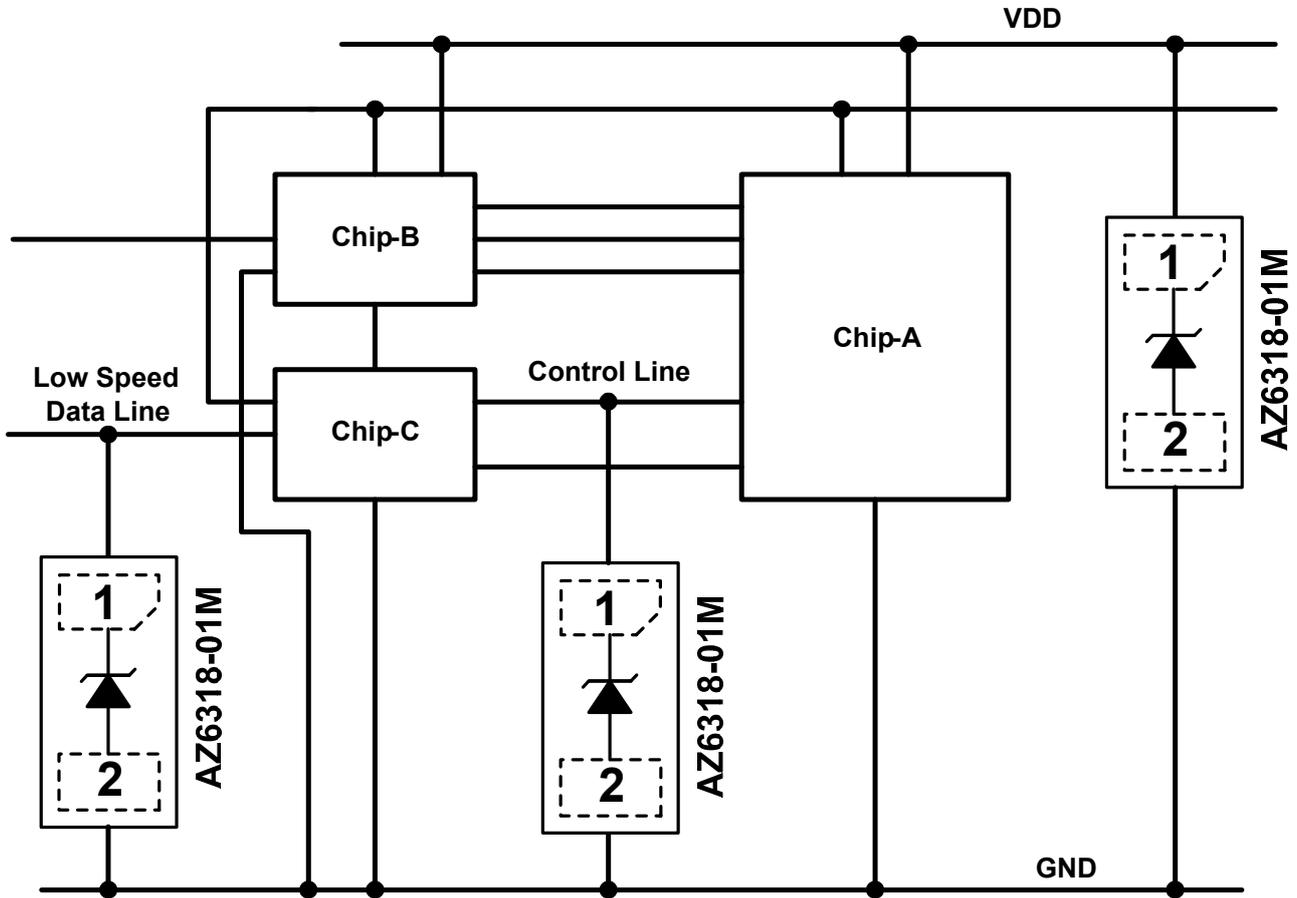
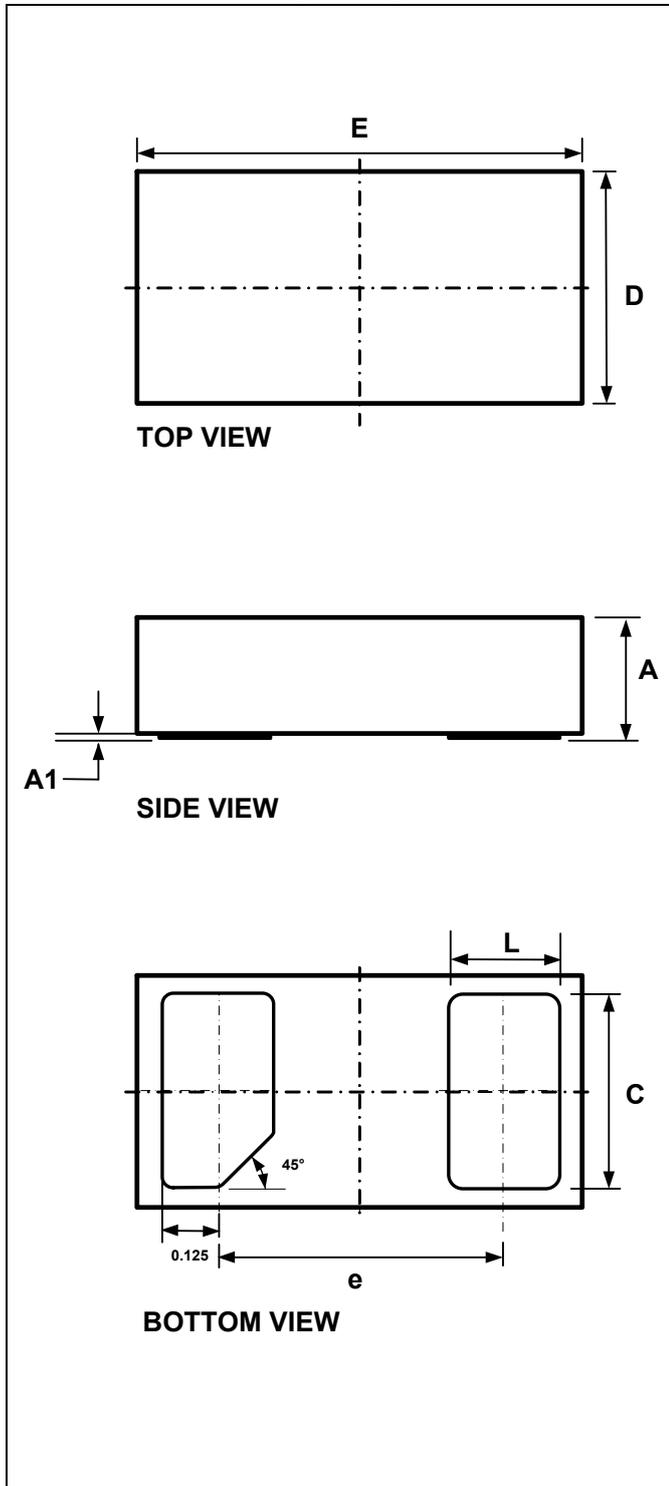


Fig. 2

## Mechanical Details

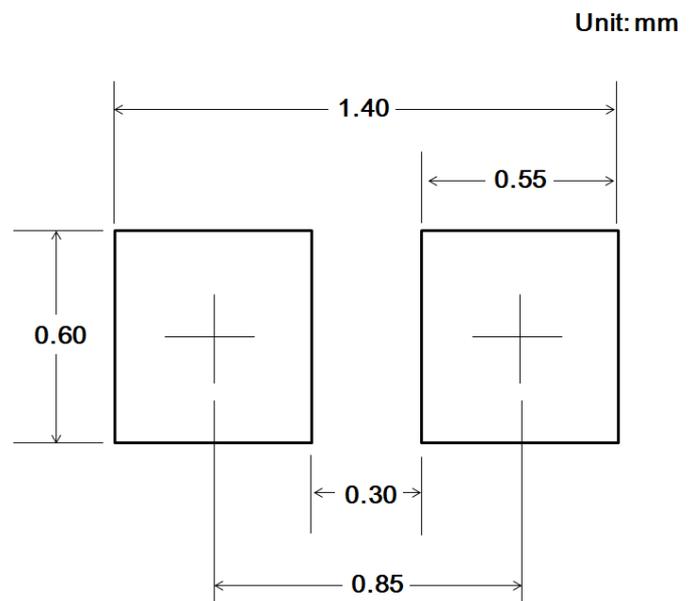
### MCSP1006P2YS Package Diagrams



### Package Dimensions

SYMBOL	MILLIMETERS		
	MIN.	NOM.	MAX.
E	0.950	1.000	1.050
D	0.550	0.600	0.650
A	0.235	0.250	0.265
A1	0.000	0.010	0.050
e	0.650 BSC		
L	0.200	0.250	0.300
C	0.450	0.500	0.550

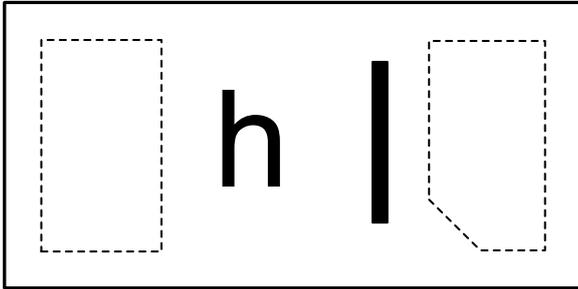
### Land Layout



#### Notes:

This LAND LAYOUT is for reference purposes only. Please consult your manufacturing partners to ensure your company's PCB design guidelines are met.

### Marking Code



Part Number	Marking Code
AZ6318-01M.R7G (Green Part)	h

h = Device Code

Note : Green means Pb-free, RoHS, and Halogen free compliant.

### Ordering Information

PN#	Material	Type	Reel size	MOQ	MOQ/internal box	MOQ/carton
AZ6318-01M.R7G	Green	T/R	7 inch	12,000/reel	4 reels = 48,000/box	6 boxes = 288,000/carton

### Revision History

Revision	Modification Description
Revision 2025/12/30	Preliminary Release.
Revision 2026/01/22	Formal Release.